**PALM INTRANET**

## Inventor Name Search Result

Your Search was:

Last Name = HONG
First Name = JIN-CHEOL

| Application# | Patent# | Status | Date Filed | Title | Inventor Name |
|---|---|---|---|---|---|
| 09750245 | 6714182 | 150 | 12/29/2000 | METHOD AND SYSTEM OF COMPENSATING KICKBACK VOLTAGE FOR A LIQUID CRYSTAL DISPLAY DEVICE | HONG, JIN-CHEOL |
| 10779660 | Not Issued | 30 | 02/18/2004 | Method of compensating kickback voltage for a liquid crystal display device | HONG, JIN-CHEOL |
| 10879344 | Not Issued | 41 | 06/30/2004 | Liquid crystal display device preventing electronic corosion and method of fabricating the same | HONG, JIN-CHEOL |

Inventor Search Completed: No Records to Display.

**Search Another: Inventor**

| Last Name | First Name | |
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| HONG | JIN-CHEOL | Search |

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| Ref # | Hits | Search Query | DBs | Default Operator | Plurals | Time Stamp |
|---|---|---|---|---|---|---|
| L1 | 387404 | (LCD OR liquid crystal display) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2005/12/20 16:03 |
| L2 | 10099 | active matrix liquid crystal display | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2005/12/20 16:04 |
| L3 | 226048 | adjust$6 SAME voltage | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2005/12/20 16:04 |
| L4 | 498 | adjust$6 SAME common adj voltage | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2005/12/20 16:05 |
| L5 | 2085738 | substrate | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2005/12/20 16:05 |
| L6 | 34122 | pixel adj electrode$1 | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2005/12/20 16:06 |
| L7 | 65858 | thin film transistor$1 | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2005/12/20 16:06 |
| L8 | 67950 | film transistor$1 | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2005/12/20 16:06 |
| L9 | 167763 | gate electrode | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2005/12/20 16:07 |
| L10 | 49079 | source electrode | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2005/12/20 16:07 |
| L11 | 98509 | data line$1 | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2005/12/20 16:07 |

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|---|---|---|---|---|---|---|
| L12 | 21650 | gate line$1 | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2005/12/20 16:08 |
| L13 | 34879 | common electrode | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2005/12/20 16:08 |
| L14 | 1135 | data line driving circuit | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2005/12/20 16:09 |
| L15 | 88326 | constant current | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2005/12/20 16:10 |
| L16 | 7655 | common voltage | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2005/12/20 16:10 |
| L17 | 3547813 | current or voltage | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2005/12/20 16:11 |
| L18 | 16871 | 1 and 5 and 6 and 7 | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2005/12/20 16:11 |
| L19 | 2873 | 1 and 5 and 6 and 7 and 9 and 10 and 11 and 12 | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2005/12/20 16:12 |
| L20 | 11 | 1 and 5 and 6 and 7 and 9 and 10 and 11 and 12 and 13 and 15 and 16 | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2005/12/20 16:15 |
| L21 | 3 | 1 and 5 and 6 and 7 and 9 and 10 and 11 and 12 and 13 and 14 and 15 and 16 | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2005/12/20 16:13 |
| L22 | 9 | 1 and 2 and 3 and 5 and 6 and 7 and 9 and 10 and 11 and 12 and 13 and 15 and 16 | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2005/12/20 16:16 |

| L23 | 3 | 1 and 2 and 4 and 5 and 6 and 7 and 9 and 10 and 11 and 12 and 13 and 15 and 16 | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2005/12/20 16:16 |
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